6-17-05

Rev. 08/22/2003 ttorney Docket No.: lient Reference No.:

MTEC-011/00US

0131.00US

PATENT

EV526724685US

Express Mail Label Number:

Date of Deposit:

EV 526724685 US June 15, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

THEKKATH et al.

Serial No.:

09/844,670

Examiner:

Anil, KHATRI

Confirmation No.:

8985

Art Unit:

2124

Filed:

April 30, 2001

For:

TRACING OUT-OF-ORDER LOAD DATA

Mail Stop Amendment

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT **TRANSMITTAL**

Enclosed is an Information Disclosure Statement and accompanying Form PTO/SB/08A for the above-identified patent application.

In accordance with 37 C.F.R. §1.97(b), no additional fee for submission of [X]the IDS is required.

The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§1.16, 1.17, and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 03-3117.

Respectfully submitted, COOLEY GODWARD LLP

Cooley Godward LLP

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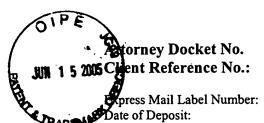
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By:

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Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97(b)

In accordance with the duty of disclosure set forth in 37 C.F.R. §1.56, Applicant(s) hereby submits the following information in conformance with 37 C.F.R. §\$1.97 and 1.98.

- [X] Pursuant to 37 C.F.R. §1.98, a copy of each document cited in the attached Form PTO/SB/08 is enclosed.
- [] No copies of the publications listed on the attached Form PTO/SB/08A are being provided pursuant to 37 C.F.R. §1.98(d) because the publications were previously cited by or submitted to the Office in prior Application Serial No. ___ to which the above-identified application claims priority under 35 U.S.C. §120.
- [] No copies of any U.S. patents or U.S. patent application publications listed on the attached Form PTO/SB/08A are being provided pursuant to 37 C.F.R. §1.98 because this application was filed after June 30, 2003.

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Attorney Docket No. MTEC-011/00US Application Serial No. 09/844,670 Page 2

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[]	[] Enclosed is a copy of a non-English publication(s) English language publication (copy enclosed) claims priority from this non-English publication.					
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Dated:	JUN	15,2005		Respectfully submitted, COOLEY GODWARD LLP		
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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet		1 of	1

Complete if Known				
Application Number	09/844,670			
Filing Date	April 30, 2001			
First Named Inventor	THEKKATH			
Group Art Unit	2124			
Examiner Name	Anil KHATRI			
Attorney Docket Number	MTEC-011/00US			

	FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document			Name of Detector on Applicant of Cited	Date of Publication		
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		OTHER – NON PATENT LITERATURE DOCUMENTS	
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	D1	Susan J. EGGERS, et al., "Techniques for Efficient Inline Tracing on a Shared-Memory Multiprocessor," University of Washington, 1990 ACM, pp. 37-47.	
	D2	Richard A. UHLIG, et al., "Trace-Driven Memory Simulation: A Survey," Intel Microcomputer Research Lab; University of Michigan, ACM Computing Surveys, Vol. 29, No. 2, June 1997, pp. 128-170.	
	D3	Eric ROTENBERG, et al., "Trace Processors," University of Wisconsin, 1997 IEEE Service Center, 12 pp.	
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	D5	Darren JONES, "Opal Coprocessor Interface," MIP Propietary/Confidential, June 4, 1999, pp. 1-18.	
	D6 .	Darren JONES, MIPSS64TM 5KCTM Processor Cores User's Manual, Rev. 1.0, July 4, 1999, pp. 6-1 to 6-26.	
	D7	MIPSS64™ 5KC™ Processor Cores User's Manual, Rev. 1.0.1., 1999, pp. 247-276.	

Examiner Signature	•	Date Considered	

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